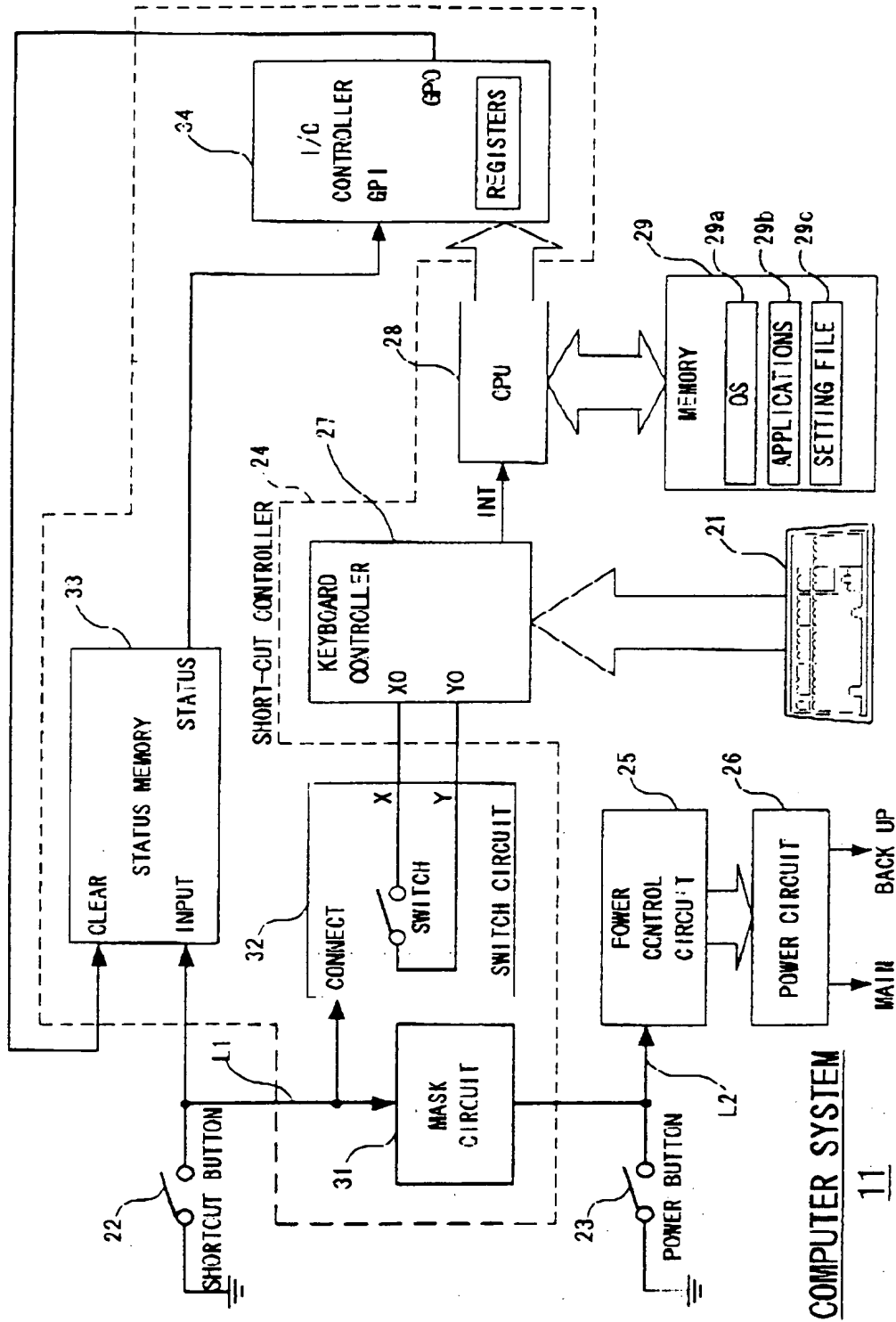


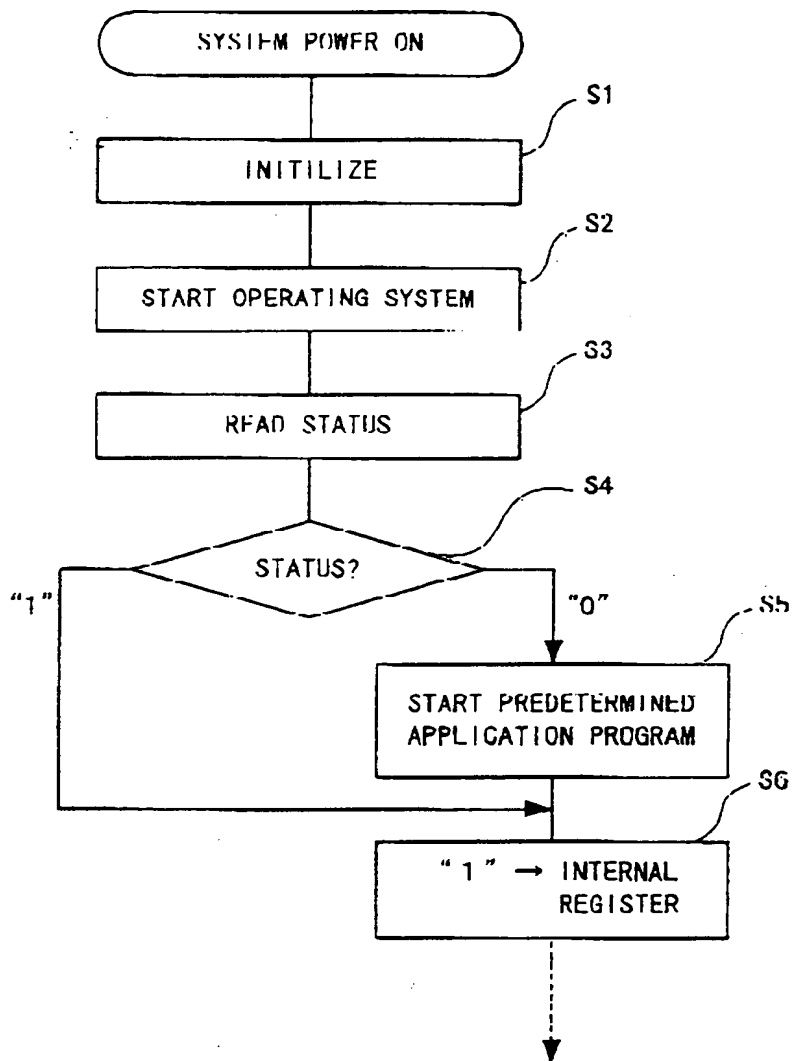
1/4
FIG. 1



COMPUTER SYSTEM

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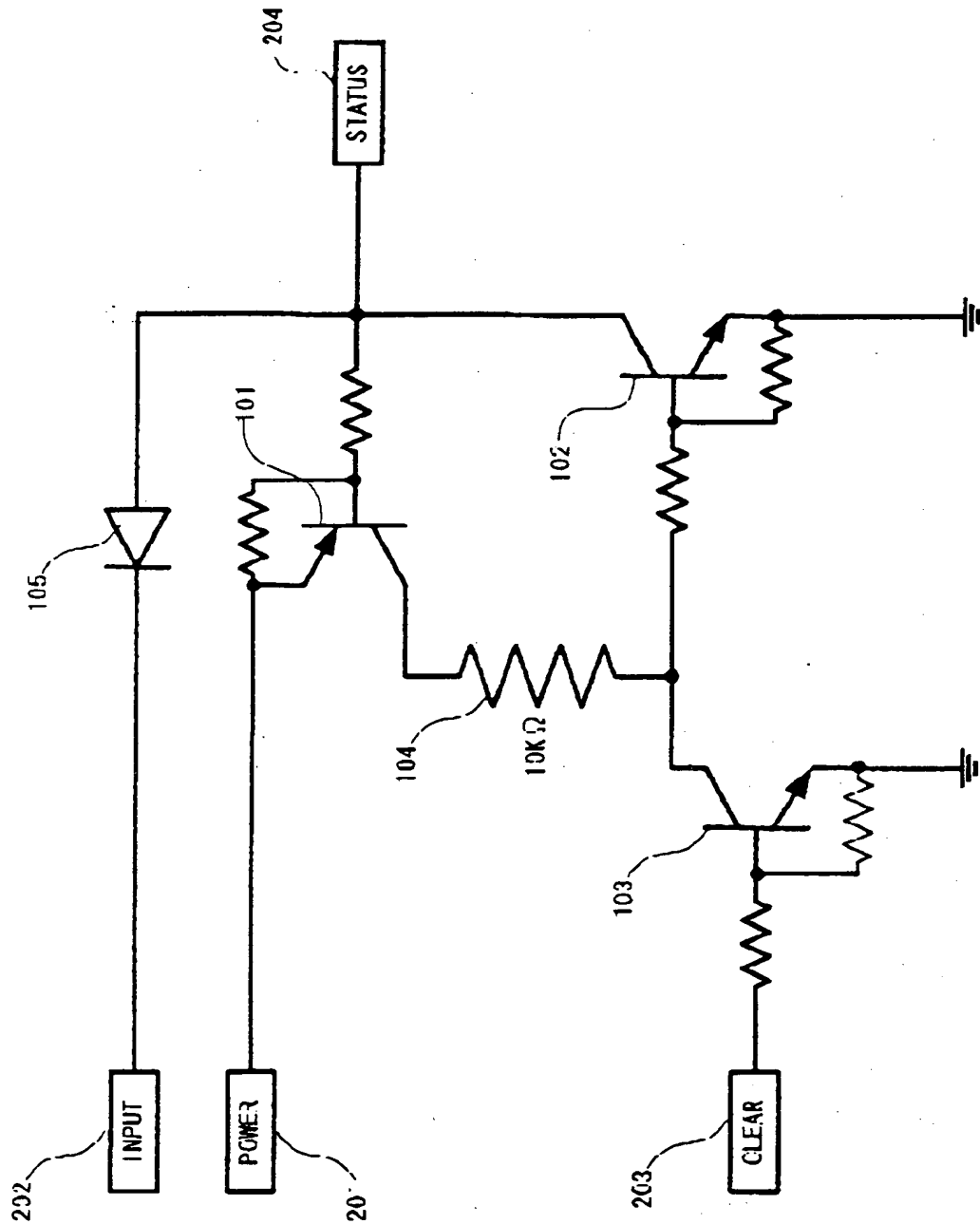
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FIG. 2



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FIG. 3



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FIG. 4

CLEAR → "1"



FIG. 4A	INPUT	Hi-Z	C
FIG. 4B	MAIN POWER	OFF	ON
FIG. 4C	STATUS	Hi-Z	0
FIG. 4D	CLEAR	C	1
FIG. 4E	DIGITAL TRANSISTOR 101	OFF	ON
FIG. 4F	DIGITAL TRANSISTOR 102	OFF	ON
FIG. 4G	DIGITAL TRANSISTOR 103	OFF	ON